AADL standards meeting February 03-06 2014

* Location Toulouse France. IRIT University.
	+ Google map link - <http://maps.google.com/maps?q=Rue+Charles+Camichel,+31000+Toulouse,+France&z=16>
	+ Address: IRIT/ENSEEIHT, 2 rue Charles CAMICHEL, 31071 TOULOUSE, FRANCE
	+ Room location: Building E+F, room F501 (fifth layer, 2nd lift right in the hall of the building).
* Not collocated with ERTS2014 - <http://www.erts2014.org/>
* Information on hotels <https://wiki.sei.cmu.edu/aadl/index.php/AADL_meetings>

# Monday February 03

* 09-0930: AADL news and notes (Bruce Lewis)
* 0930-1000: Design and Analysis of Multicore systems with AADL (Julien Delange and Peter Feiler)
* 1000-1030: Modeling and Scheduling Analysis for Various Types of multi processors and multi-core Architectures (Stephane Rubini)
* 1030-1100: break
* 1100-1130: Ellidiss projects updates and technology advances (Pierre Dissaux)
* 1130-1200: Real-Time Scheduling Analysis with Cache Handling Support (Nam Hai Tran)
* 1200-1230: Discussions on guidelines for multicore (Peter Feiler and Julien Delange)
* 1230-0200: Lunch
* 0200-0230: Subset Annex (Vincent Gaudrel)
* 0230-0300: Constraints Annex Implementation Approach (Jerome Hugues)
* 0300-0330: Experiments with Constraints and the Requirements Annex (Peter Feiler)
* 0330-0400: Constraints Annex updates (Serban Gheorghe)
* 0400-0430: break
* 0430-0530: Continuous constraints annex
* 0530-0600: OSATE2 updates (Julien Delange)

# Tuesday February 04

* 0900-0945: AADL ARINC653 code generation Status Report (Etienne Borde)
* 0945-1030: Integrating Model-Based Analysis to support verifiable system composition Status Report (Jerome Hugues)
* 1030-1100: break
* 1100-1130: AADL to VHDL transformation (Dominique Blouin)
* 1130-1200: Requirements Annex updates (Dominique Blouin)
* 1200-1230: ADELE OSATE demonstration (Dominique Blouin)
* 1230-0200: lunch
* 0200-0230: System to Software Integrity – Generation from AADL to Spark/Ada - Project Report (Jerome Hugues)
* 0230-0300: Design Space Exploration: Bridging the Gap Between High-Level Models and Virtual Execution Platforms (Jerome Hugues)
	+ This paper presents a novel embedded systems modeling framework that fills the gap between high-level AADL models and low-level hardware virtual execution platforms. This approach allows refinement and improvement of system performance through exploration of architectures at different levels of abstraction. The aim of the proposed approach is to achieve virtual prototyping of the complete system in order to allow validation to begin early in the design flow, thereby accelerating its development while improving system performances. The proposed framework is validated using an MJPEG video decoder application. Experimental results show how virtual prototyping allows the system architect to fine-tune performances by reallocating tasks between processors and/or retargeting tasks to hardware modules. Fives candidate architectures have been explored to find an optimal solution that delivers 12 times the performances compared to an all-software mapping.
* 0300-0400: Error-Modeling Annex Ballot (Peter Feiler)
* 0400-0430: break
* 0430-0530: continued error-model annex
* 0530-0600: Errata for behavior annex (Etienne Borde)
* 0600-0630: Suggested Updates to Arinc653 Annex (Etienne Borde)

# Wednesday February 05

* 0900-1030: errata discussion for AADL (Peter Feiler)
* 1030-1100: break
* 1100-1130: continued errata discussion
* 1130-1230: Annex for unit consistency and basic expression (Alexey Khoroshilov)
* 1230-0200: lunch
* 0200-0300: AADL guidelines for ARINC664 switched networks (Alexey Khoroshilov)
* 0300-0400: Synchronous Annex proposal (Jean-Pierre Talpin)
* 0400-0430: break
* 0430-0500: continued synchronous annex
* 0500-0530: AADLv2 transformation to FIACRE (Marc Plantel)
* 0530-0600: planning for the next meeting (Bruce Lewis)

# Thursday February 06

* 0900-1030: Tutorial with AADL inspector 1.3 (Pierre Dissaux) (Please let us know if you plan to attend – may cancel to do later based on current response)
* 1030-1100: break
* 1100-1230: tutorial

# WEBEX Meeting

Topic: AS2C AADL Committee

Date: Every 1 day, from Monday, February 3, 2014 to Thursday, February 6, 2014

Time: 8:30 am, Europe Time (Paris, GMT+01:00) Meeting Number: 659 070 322 Meeting Password: AS2Caadl

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