AADL standards meeting September 29-October 2 2014

* Location Valencia, Spain
* After the MoDELS conference with the AADL tutorial on September, 28 afternoon and the AADL workshop during September, 29
  + See the conference website: <http://modelsconference.org/>
  + See the workshop website: <http://www.aadl.info/aadl/avci/>
* Location: Ciudad Politécnica de la Innovación - Universidad Politécnica de Valencia. - Innova Room (Green Cube)
* Hotels: please look at the hotels recommendation list on the MoDELS conference website <http://modelsconference.org/>

# Monday September 29 – Architecture-Centric Virtual Integration Workshop

The Architecture-Centric Virtual Integration Workshop is a satellite event of the MoDELS conference. To attend, you need to register to the conference and satellites events at <http://modelsconference.org/>

The workshop features many publications and tutorials about AADL-related work. The workshop will start with a keynote by Peter Feiler on “The Story of AADL”. This will be a unique opportunity to discuss work and research projects related to AADL. The complete schedule for the day is available at <http://www.aadl.info/aadl/avci/program.html>

If you have any question, please contact the organizers at <http://www.aadl.info/aadl/avci/contact.html>

# Tuesday September 30

* 0900-0930: AADL news and notes (Bruce Lewis)
* 0930-1000: OSATE2 updates (Julien Delange)
* 1000-1030: Synchronous Systems Draft (Jean-Pierre Talpin)
* 1030-1045: break
* 1045-1200: Synchronous Systems Draft (Jean-Pierre Talpin)
* 1200-0100: Error-Model v2 Ballot (Peter Feiler)
* 0100-0230: lunch
* 0230-0300: ARINC653 Annex Ballot (Julien Delange)
* 0300-0330: Code Generation Annex (Jerome Hugues)
* 0330-0345: break
* 0345-0430: Cyber-Physical Systems Integration Consistency (Dionisio De Niz)
* 0430-0530: AADL Behavior Annex Errata – Etienne Borde
* 05:30-0600: AADL ARINC653 Code Generation Report – Etienne Borde

# Wednesday October 1

* 0900-1030: AADL v2.1 errata (Peter Feiler)
* 1030-1045: break
* 1045-1200: AADL v2.2 roadmap (Peter Feiler)
* 12:00-1230: : Integrating Model-Based Analysis to Support Verifiable System Composition Report (Jerome Hugues)
* 12:30-0100: Requirements Annex Update ( Dominique Blouin)
* 0100-0200: lunch
* 0200-0300: Network Annex Update (Alexey Khoroshilov, Brendan Hall, Tiyam Robati)
* 0300-0330: Update on UAH Graphics for OSATE (Philip Alldredge)
* 0330-0345: break
* 0345-0500: Hybrid Annex update (Ehsan Ahmad, Naijun Zhan, Brian Larson)
* 0500-0530: Planning for Next Meeting (Bruce Lewis)

# Thursday October 2

* 0900-1030: TASTE tutorial (Pierre Dissaux)
* 1030-1045: break
* 1045-1230: TASTE tutorial (Pierre Dissaux)