AADL standards meeting Oct 3-5, 2016

* Location Pittsburg, PA, USA.
	+ Meeting information
		- At the Software Engineering Institute in Pittsburgh
		- In conjunction with the Embedded Systems Week
		- Sunday morning, Oct 2, tutorial by Julien on Safety and Security, $50 to ESweek
		- Joint workshop with AdaEurope Oct 6,7
	+ No AADL Conference or Meeting fee – tutorials do not require general conference fee, only the fees for workshops/tutorials

# Monday, Oct 3

* 0900-1000: AADL standardization committee news + action items (Bruce Lewis)
* 1000-1030: AADL v3 roadmap review (Peter Feiler)
* 1030-1100: break
* 1100-1230: AADL v3 discussions (Nested processors, virtual memory & memory configurations) (Peter Feiler, Alexey Khoroshilov, Jerome Hugues)
* 1230-1400: Lunch
* 1400-1430: Continued (Nested processors, virtual memory & memory configurations)
* 1430-1530: AADL v3 discussions (Configuration & Binding) (Peter Feiler)
* 1530-1600: Break
* 1600-1630: Continued (Configuration & Binding)
* 1630-1700: SCADE AADL Capabilities (ANSYS)
* 1700-1730: Cheddar Update (Frank Singhoff)

# Tuesday, Oct 4

* 0900-1030: AADL v3 discussions (Compositional Interfaces) (Peter Feiler)
* 1030-1100: break
* 1100-1130: Continued (Compositional Interfaces)
* 1130-1230: AADL v3 discussions (array connections, unified type systems)
* 1230-1400: Lunch
* 1400-1500: Continued (array connections, unified type systems)
* 1500-1530: Break
* 1530-1630: Security Annex Discussion (Julien Delange)
* 1630-1700: Update on OCARINA (Jerome Hugues)

# Wednesday, Oct 5

* 0900-1030: Network Annex Draft Review and Update (Alexey Khoroshilov, Tiyam Robati, Brendan Hall)
* 1030-1100: break
* 1100-1230: AADL Core Errata (Peter Feiler)
* 1230-1400: Lunch
* 1400-1500: Comments on Behavior Annex Ballot (Etienne Borde)
* 1500-1530: Break
* 1530-1630: Update on integration of BLESS/Formal Semantics with BA (Etienne Borde, Jean-Pierre, Brian Larson)
* 1630-1700: Planning for next meeting (Bruce Lewis)

# Webex Info for the meeting: