AADL standards meeting Jan 30-Feb2, 2017

* Location Toulouse, France.
  + Meeting information
    - At the IRIT, Institut de Recherche en Informatique de Toulouse – CNRS
    - Rue Charles Camichel, 31000, Toulouse
  + <http://maps.google.com/maps?q=Rue+Charles+Camichel,+31000+Toulouse,+France&z=16>
  + Please send Marc your name, nationality, current residence and email so he will have you on the list to enter the campus.

# Monday, Jan 30

* 0900-1000: AADL standardization committee news + action items (Bruce Lewis)
* 1000-1030: AADL v3 roadmap review (Peter Feiler)
* 1030-1100: break
* 1100-1230: AADL v3 discussions (Nested processors, virtual memory & memory configurations) (Peter Feiler, Alexey Khoroshilov, Jerome Hugues)
* 1230-1400: Lunch
* 1400-1430: Continued (Nested processors, virtual memory & memory configurations)
* 1430-1530: Network Annex Draft Review and Update (Alexey Khoroshilov, Tiyam Robati, Brendan Hall)
* 1530-1600: Break
* 1600-1630: Network Annex discussion continued.
* 1630-1700: Hybrid Annex discussion and Physical Modeling (Brian Larson, Pavi)

# Tuesday, Jan 31

* 0900-1030: AADL v3 discussions (Compositional Interfaces) (Peter Feiler)
* 1030-1100: break
* 1100-1130: Continued (Compositional Interfaces)
* 1130-1230: AADL v3 discussions (array connections, unified type systems)
* 1230-1400: Lunch
* 1400-1500: Continued (array connections, unified type systems)
* 1500-1530: Break
* 1530-1600: AADL v3 discussions (Configuration & Binding) (Peter Feiler, Alexey Khoroshilov, Denis Buzdalov)
* 1600-1630: Approaches to FMI with AADL (Jerome Hugues, Raphael Faudou)
* 1630-1700: Considerations for Cyber Physical Systems (Peter Feiler)

# Wednesday, Feb 1

* 0900-1000: Demonstration of BA formal semantics Project (Jean-Pierre Talpin)
* 1000-1030: Merging BLESS and JP’s formal semantics for SCADE Expression (Brian Larson)
* 1030-1100: break
* 1100-1130: Merging BLESS continued
* 1200-1230: Discussion of next steps (Etienne Borde)
* 1230-1400: Lunch
* 1400-1430: INGEQUIP Capella to AADL transformation Refinement and Analysis using STOOD and AADL Inspector (Pierre Dissaux, Pierre Gaufillet)
* 1430-1500: Ellidiss Update on AADL tools, technology, and Projects (Pierre Dissaux)
* 1500-1530: Break
* 1530-1630: AADL Core and EMV2 Errata (Peter Feiler)
* 1630-1700: Core Errata continued, semantics of XOR in EMV2 white paper
* 1700-1730: Planning for next meeting (Bruce Lewis)

# Thursday, Feb 2

* 0900-0930: Assurance Cases – Integration with the Requirements Annex (Peter Feiler)
* 0930-1130: ALISA Tutorial (Peter Feiler)

# Webex Info for the meeting:

Monday:

Tuesday:

Wednesday:

Thursday: