AADL Standards Meeting June 24-27, 2019

* Location Paris, France
	+ - Hosted by Telecom-Paristech, Etienne Borde and Dominique Blouin
		- The school is located at 46 Barrault street in Paris 13 district ([https://www.google.fr/maps/place/T%C3%A9l%C3%A9com+ParisTech/@48.8262943,2.3442301,17z/data=!3m1!4b1!4m5!3m4!1s0x47e671975e9d2e3d:0x17f35deee8ffbbf1!8m2!3d48.8262943!4d2.3464188](https://www.google.fr/maps/place/T%C3%A9l%C3%A9com%2BParisTech/%4048.8262943%2C2.3442301%2C17z/data%3D%213m1%214b1%214m5%213m4%211s0x47e671975e9d2e3d%3A0x17f35deee8ffbbf1%218m2%213d48.8262943%214d2.3464188)).
		- Directions to reach the school from airports can be found here: <https://www.telecom-paristech.fr/eng/practical-information/getting-there.html>
		- Paris Air Show - The 53rd International Paris Air Show will take place from **17 to 23 June 2019**at the Exhibition Center of Le Bourget, a few kilometers North of Paris.
		- “Bye Bye Barrault” Party, June 29th

<https://www.facebook.com/events/t%C3%A9l%C3%A9com-paristech-alumni/bye-bye-barrault/324353694906324/> (University is moving)

* + - All times noted are for Paris, France

# Monday, June 24

* 0900-1000: AADL standardization committee news + action items (Bruce Lewis)
* 1000-1030: AADL v3- Roadmap (Peter Feiler)
* 1030-1100: break
* 1100-1230: Review Part 1 Generic architecture description of AADL (Peter Feiler)
* 1230-1400: Lunch
* 1400-1530: Review Part 1 Generic architecture description of AADL (Peter Feiler)
* 1530-1600: Break
* 1600-1730: Tooling and instance model for V3 (Wrage, Hugues)

# Tuesday, June 25

* 0900-1030: AADL v3- Type system & Expression language (Wrage)
* 1030-1100: break
* 1100-1230: Part 2/3 Component semantics (Hugues)
* 1230-1400: Lunch
* 1400-1530: Security Annex (Dave Gluch)
* 1530-1600: Break
* 1600-1730: Property definition, assignment, property sets (Feiler)
* 1730-1800: Network Annex Update (Alexey Khoroshilov, Brendan Hall)

# Wednesday, June 26

* 0900-1030: Errata V2 / EMV2
* 1030-1100: break
* 1100-1230: Revision of Annex concept (Hugues, Wrage)
* 1230-1400: Lunch
* 1400-1430: Binding concept revision (Feiler)
* 1430-1500: Ellidiss LAMP Tutorial (Pierre Dissaux)
* 1500-1530: TBD – related to MCP
* 1530-1600: Break
* 1600-1800: Suggestions for AADL Specification of Multi-Core Processors (Mark Brown)

# Thursday, June 27

* 0900-1000: Multi-Paradigm Modeling (Hans Vangheluwe)
* 1000-1100: IRT SystemX Demo with AADL (Dominique Blouin, Francoise Caron, Paolo Crisafully, Cristian Maxim)
* 1100-1115: Break
* 1115-1200: Model Management for AADL (Dominique Blouin)

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# Webex Meeting information:

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